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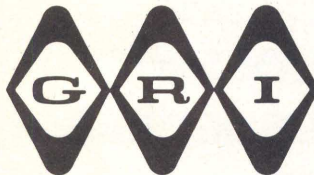


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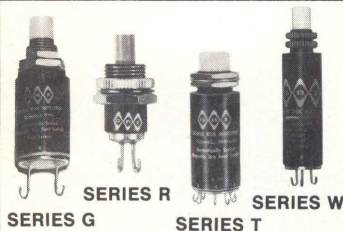
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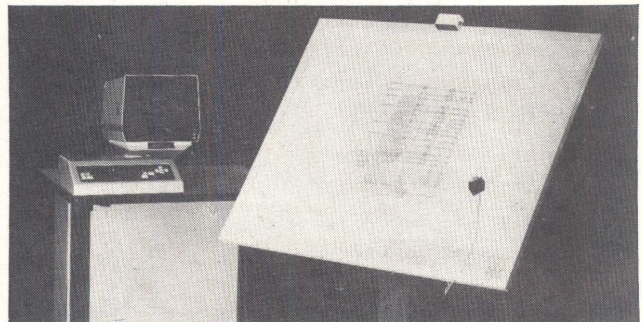
PRODUCTS

MINICOMPUTER WITH HIGH LEVEL LANGUAGE

Hitac 20 employs PL/I, edited for minicomputer applications and renamed PLUS (Programming Language for User's System). Microprogramming control and wide use of LSI are featured; and floating-point arithmetic adapter, hardware debug adapter, and I/O control adapter are all modular. Core memory processor has a 16-bit + 2-parity word length, 650-ns cycle time, and 8K- to 64K-word capacity; ACU has 90 (122 optional) 1- or 2-word instructions, 16 g-p registers, and 64K-word direct/relative/base/index/immediate addressing. Floating-point execution times are 32/64 bits addition/subtraction, 9 to 15 and 10 to 23 μ s, respectively; 32/64 bits multiplication/division, 12 to 16 and 17 to 28 μ s, respectively. Disc operating system, full utility, debug hardware, and console display are included. **Hitachi Ltd.**, New Marunouchi Bldg, No. 5-1, 1-chome, Marunouchi Chiyoda-ku, Tokyo 100, Japan.

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LOW COST DIGITIZING TERMINAL



Large Tablet Digitizer (LTD), for input of graphic data to the company's DesignerTM interactive graphics systems, has a 35 x 47" active digitizer surface, including menu area. The head has no mechanical connection to the table, allowing freedom of operator hand movement. Any point on the surface has absolute, permanently defined coordinates. If power is interrupted, or if the operator lifts the cursor from the table, coordinates are not lost. Both puck and pen cursors are available, as are an echo CRT (11 or 19" diag display screen) for checking and interactive editing; 12" sq sketch pad tablet; TelewriterTM keyboard/thermal printer for operator commands, error message, and data printing; and a 16-key program function keyboard. **The Computervision Corp.**, 201 Burlington Rd, Bedford, MA 01730.

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MOS/CMOS PROTECTION DEVICES

TransZorbTM transient voltage suppressors for bipolar, MOS, and Schottky ICs are supplied in nine voltages from 5 to 45 V at std power supply levels and offer subnanosecond response, high surge capabilities (15 kW for 10 μ s), and low clamping ratio. With internal system disturbances, devices prevent catastrophic failure or slow degradation of the input circuit. With external, protection is provided by placing a device in the power supply line; having a low "on" resistance, it will effectively short out unwanted transients while maintaining circuit voltage level. Max ratings include 1500-W pk pulse power dissipation at 25°C; op/storage temps of -65 to 175°C; forward surge of 200 A, 1/20 s at 25°C; steady-state power dissipation of 1 W; and repetition rate of 0.01%. **General Semiconductor Industries, Inc.**, 2001 W Tenth Pl, Tempe, AZ 85281.

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